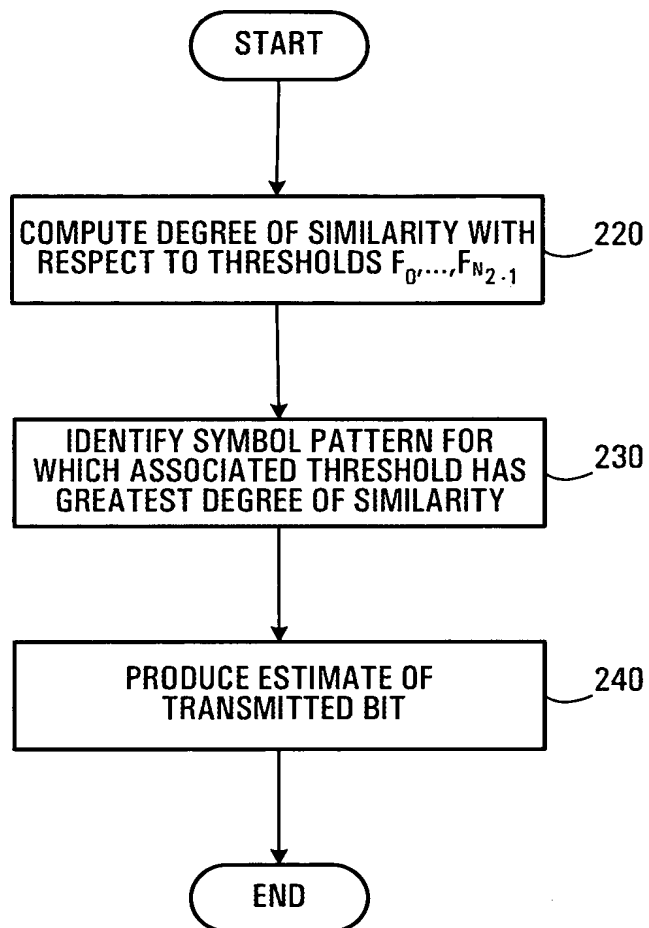
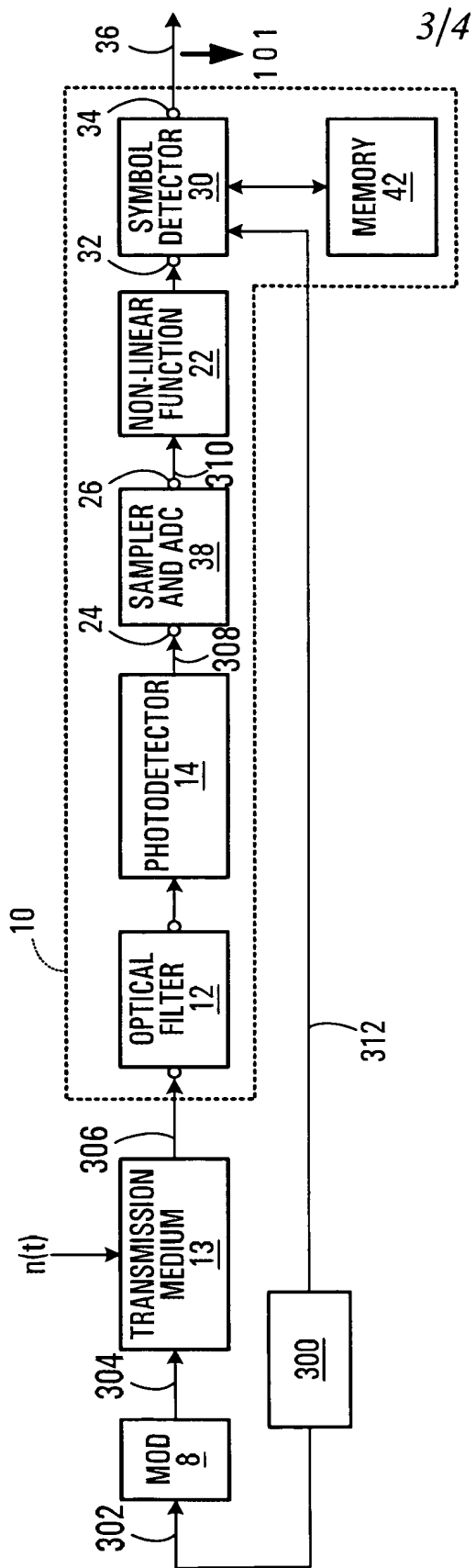


FIG. 1

**FIG. 2**

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FIG. 3

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SYMBOL PATTERN	THRESHOLD
0 0 0 0	$F_0$
0 0 0 1	$F_1$
0 0 1 0	$F_2$
0 0 1 1	$F_3$
0 1 0 0	$F_4$
$\vdots$	$\vdots$
1 1 1 1	$F_{2^N-1}$

$L2$   $\underbrace{\hspace{1.5cm}}$   $L1$   
 $K^{TH}$  BIT  
 POSITION  
 $\underbrace{\hspace{1.5cm}}$   
 $N$

FIG. 4